

<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/730,600	NEWTON ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Ali Alavi	2875	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to application filed on 12/08/03.
2. ☒ The allowed claim(s) is/are 1-60.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |  |  |
|--|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application                      |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO/SB/08),<br>Paper No./Mail Date <u>12/08/03 &amp; 8/09/07</u> | 7. <input type="checkbox"/> Examiner's Amendment/Comment                               |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material                                 | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|  | 9. <input type="checkbox"/> Other _____.   |

### Reasons For Allowance

The following is an examiner's statement of reasons for allowance:

Claim 1 is allowed because the prior art of record fails to teach or suggest an array of LED emitters structure in which having at least first and second rows of LED emitters on a substrate and electrically coupled together in a circuit, the rows separated by a first predetermined spacing, the LED emitters in each row having predetermined emission beam widths, and the emission axis of each LED emitters parallel with the emission axes of the other LED emitters in the same row, wherein the emission axes of the LED emitters of the second row are oriented at a predetermined non-zero angle relative to the emission axes of the first row of the LED emitters, and each emitters in each row of the LED emitters is separated from an adjacent LED emitter by a second predetermined spacing.

Claims 2-26 and 28-29 are allowed because being depended on base claim 1.

Claim 27 is allowed because the prior art of record fails to teach or suggest an LED task light structure in which having an array of LED emitters, each LED emitter (LED) having an emission beam width and mounted on a substrate including: a plurality of LEDs electrically connected in an LED circuit and disposed in at least first and second parallel columns, each column defining an LED emitter axis wherein the LED emitter axis of the first column of LEDs is oriented perpendicular to the substrate, each column separated on the substrate from an adjacent column by a first predetermined spacing, each LED in each column separated on the substrate from an adjacent LED in the column by a second predetermined spacing, and each column of LEDs oriented in a

direction displaced by a predetermined non-zero angle relative to the LED orientation in an adjacent column, wherein the predetermined non-zero angle is a defined fraction of the emission beam width of an individual LED, an electrical drive circuit connected to the LED circuit, mounted on the substrate and providing a pulsating DC voltage sufficient to drive the LEDs in the LED circuit to a maximum peak intensity; and a light-transmissive housing for enclosing and supporting the substrate having the LED array and the electrical drive circuit.

Claims 30-46 are allowed because being depended on base claim 27.

Claim 47 is allowed because the prior art of record fails to teach or suggest an LED light source assembly structure, having a substrate defining an emission plane a first plurality of LED emitters mounted on the substrate, arranged in a first line such that the emission axes of all LED emitters in the first line are parallel and disposed in a first plane defined by the first line and the parallel emission axes of the first plurality of LED emitters, and a second plurality of LED emitters mounted on the substrate, arranged in a second line parallel to the first line and offset by a first predetermined spacing from the first line such that the emission axes of all LED emitters in the second line are parallel and disposed in a second plane defined by the second line and the parallel axes of the second plurality of LED emitters, wherein the second plane is disposed at a predetermined non-zero angle relative to the first plane.

Claims 48-60 are allowed because being depended on base claim 47.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communication from the examiner should be directed to Ali Alavi whose telephone number is (571) 272-2365. The examiner can normally be reached between 7:00 A.M. to 5:30 P.M. Tuesday to Friday. If attempts to reach the examiner by phone are unsuccessful, the examiner's supervisor, Sandy O'Shea can be reached at (571) 272-2378 or you may fax your inquiry to the **Central Fax at (571) 273-8300**.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

AA

/Ali Alavi/

Primary Examiner